Q.P. Code: 16EE4305													R16		
Reg. No.													]		
SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR (AUTONOMOUS) M.Tech I Year I Semester Regular & Supplementary Examinations February 2018 NEURAL NETWORKS AND FUZZY LOGIC (Power Electronics)															
Time: 3 hours Max. Marks: 60 (Answer all Five Units <b>5 X 12 =60</b> Marks)															)
1	a	Explain	about	the M	lc Cul	loch-F	_	NIT-I							6M
	b	Explain		6M											
2	а	Describe	e the ap	pplicat	tions o	f ANN	٨.	OR							6M
	b	What are	e the l	earnin	ig stra	tegies	for ar	tificial	neura	l netw	orks?				6M
3	0	Evoluin	about	the be	alt nr	0.000		NIT-I							
3		Explain Explain			-	10					NE" n	nodel			6M
	U	Explain	the arc	meet	urarud		inu aig	OR		DALI		louei			6M
4		Explain computations in multi layer feed forward networks													
5		<b>UNIT-III</b> Explain about the training algorithms for pattern association.													
6	a	Write sh	nort no	otes on	1 Hopf	ïeld n	etworl	OR KS.							6M
		b Explain about the bidirectional associative memory.													6M 6M
_			_					NIT-I	V						0101
7		Explain		-			•	sets.							6M
	b	b Explain about the fuzzy relations <b>OR</b>										6M			
8		Explain	decisio	on mal	king u	sing fu	izzy co		tion oj	peratio	ns.				12M
9		What is	fuzzif	ïcatio	n? Exj	plain a		NIT-V he def OR		cation	to cris	sp sets	5		
10	a	Explain	about	the fu	ızzy lo	ogic ba	ased u	nit cor	nmitm	ent					6M
	b	Explain	about	the lo	ad flo	w stuc	ties.								6M

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